



FIG. 1

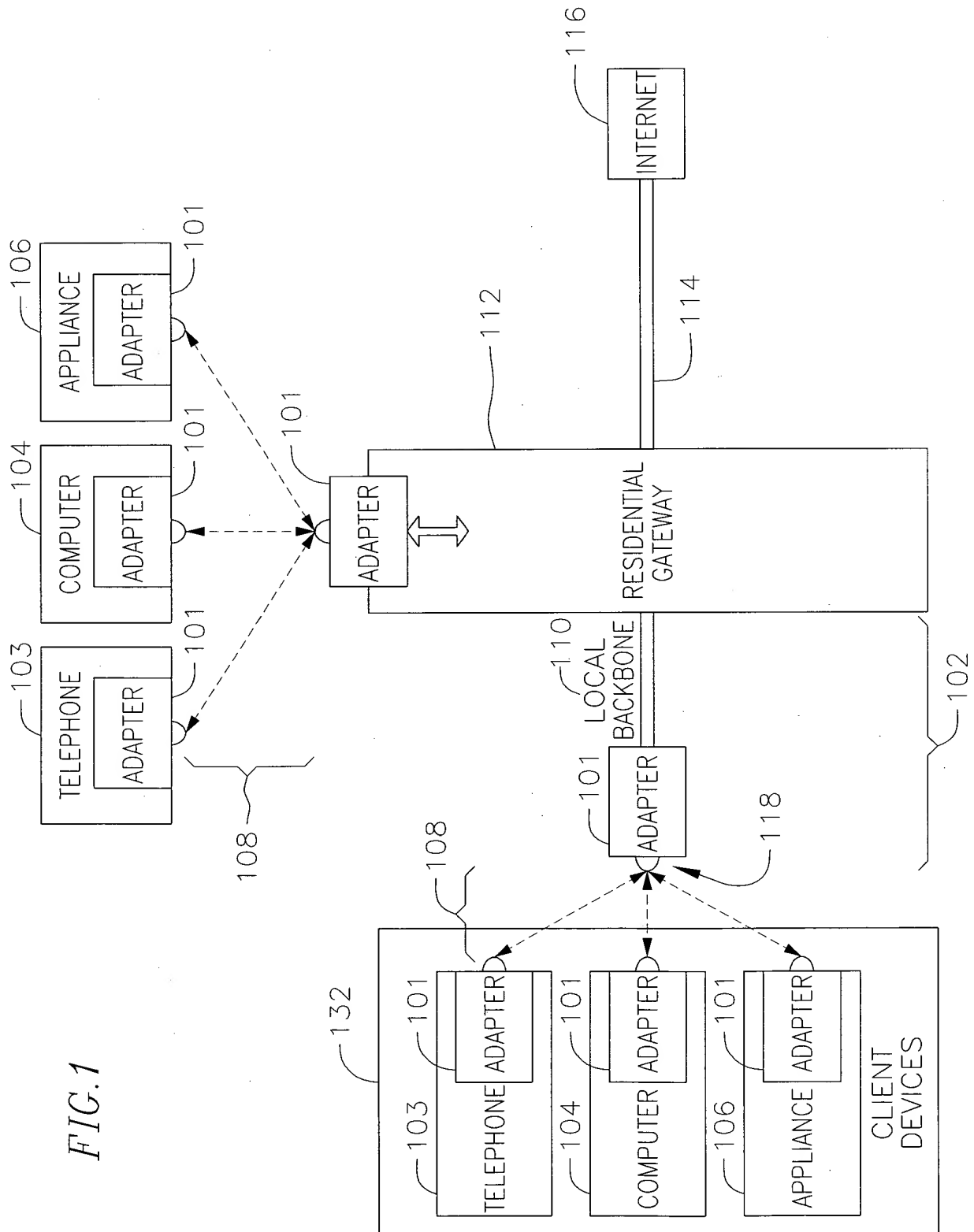


FIG. 2

EBI EXPANSION

EEPROM (OPTIONAL)

SRAM (OPTIONAL)

FLASH

16-BIT EXTERNAL BUS INTERFACE

140 MHz 16-BIT DUAL MAC DSP CORE

48K WORDS DATA SRAM

80K WORDS PROGRAM SRAM

PCM/IOM-2

CHO

CH1

CH2

CH3

HOOK DETECT

AUDIO PROCESSOR

VOICE AFE WITH 14BIT A/D-D/A

4-WIRE DIFFERENTIAL AUDIO

GPIO

SLIC CONTROL

LEDs/MISC

PERIPHERAL BUS

INTERRUPT CONTROL

DUAL UART

TIMERS

PB BRIDGE

IPSEC ENCRYPTION ENGINE

DMA

DPS/SB INTERFACE

DPS/PB INTERFACE

16/32-BIT SDRAM INTERFACE

256-MB 80MHz SDRAM

1108

HPNA AFE

802.3u

USB 1.1 TWISTED PAIR

0-65 MHz RF

AMP Ctl

36/44MHz IF

TUNER Ctl

36/50MHz OOB Rx

1100

QPSK OOB DOWNSTREAM DEMODULATOR WITH 6 BIT A/D

64/256QAM DOWNSTREAM DEMODULATOR WITH 10 BIT A/D

QPSK/16QAM UPSTREAM MODULATOR WITH 10 BIT D/A

USB 1.1 TRANSCEIVER

1 LINE 20 HPNA MAC

802.3u ETHERNET MAC

USB 1.1 MAC

ENHANCED DOCSIS 1.1 MAC WITH BPI+

OOB DOWNSTREAM PROCESSOR

80MHz r3000 MIPS CORE WITH MIPS32 EXTENSIONS

4 KB I-CACHE

4 KB O-CACHE

1106

1100

1102

1104

1106

1108

1110

1112

1114

1116

1118

1120

1122

1124

1126

1128

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1802



FIG. 3

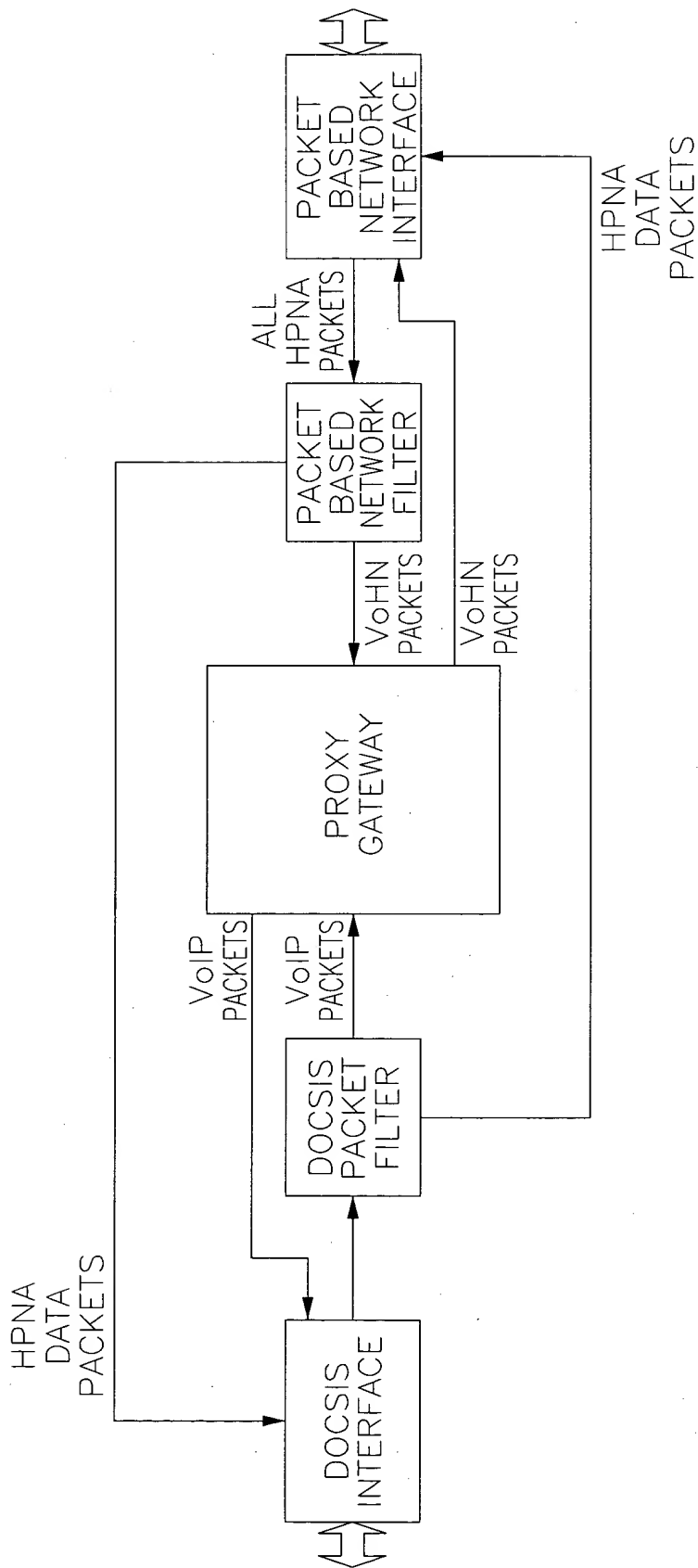
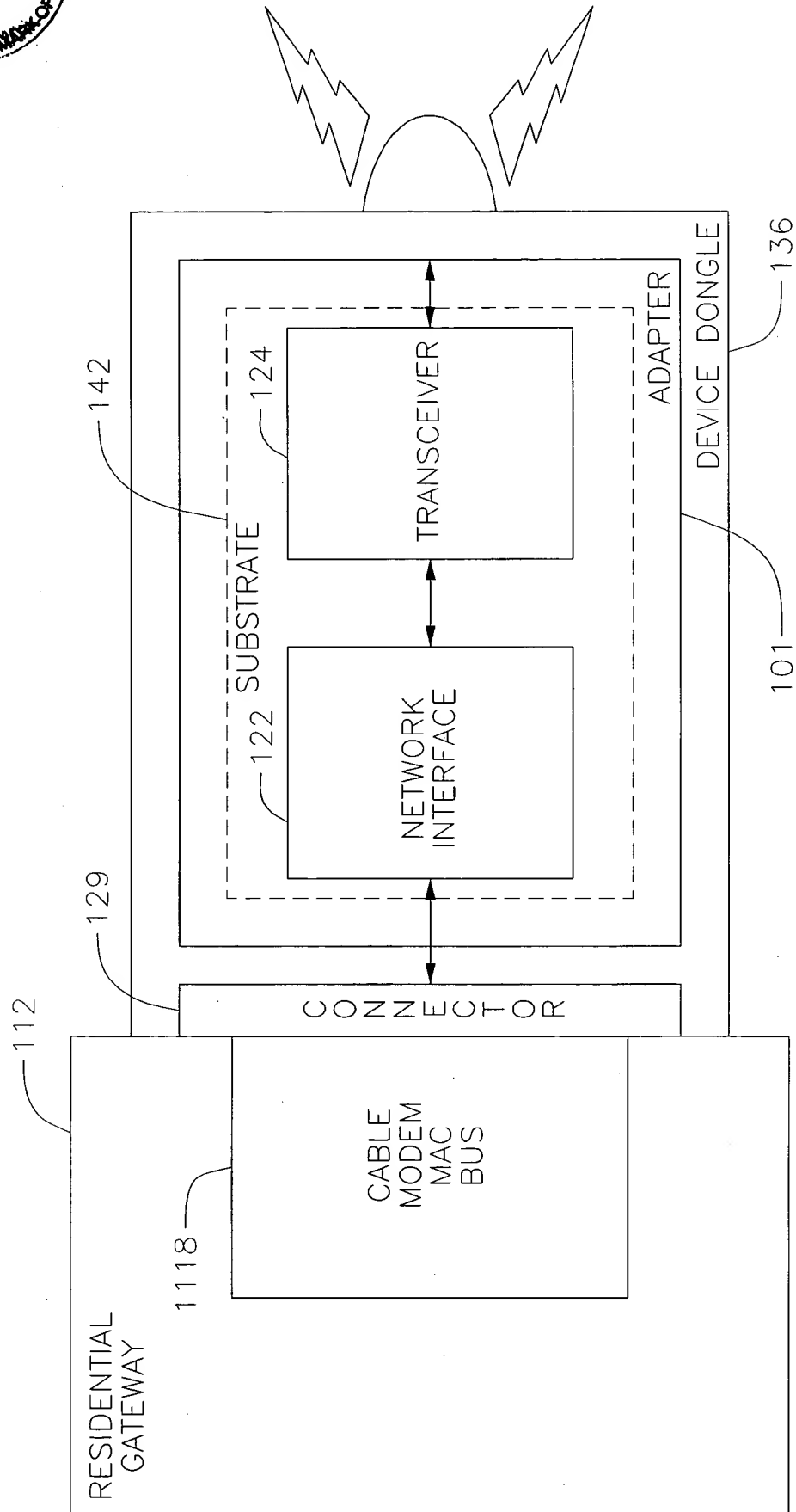




FIG. 4



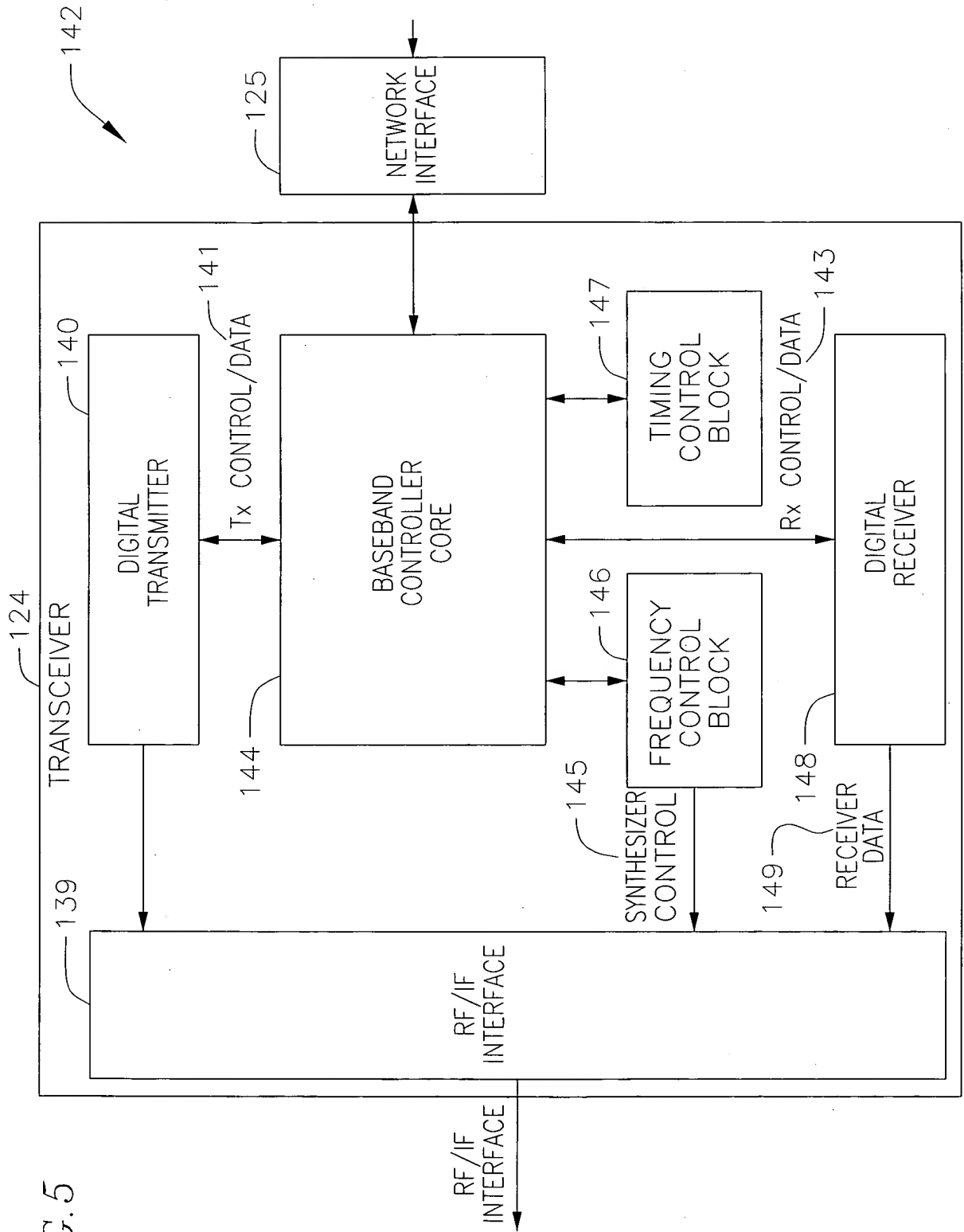
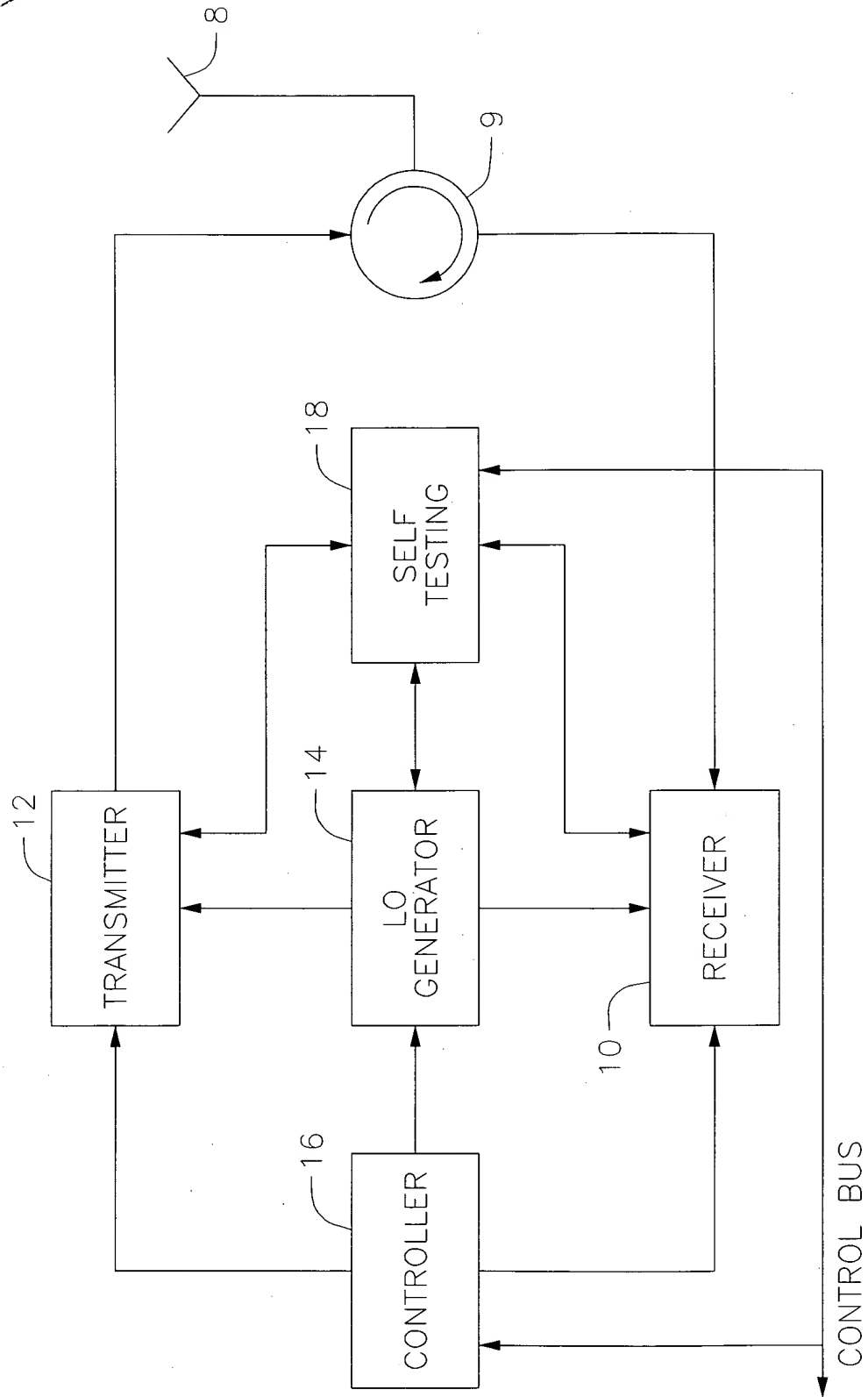




FIG. 6



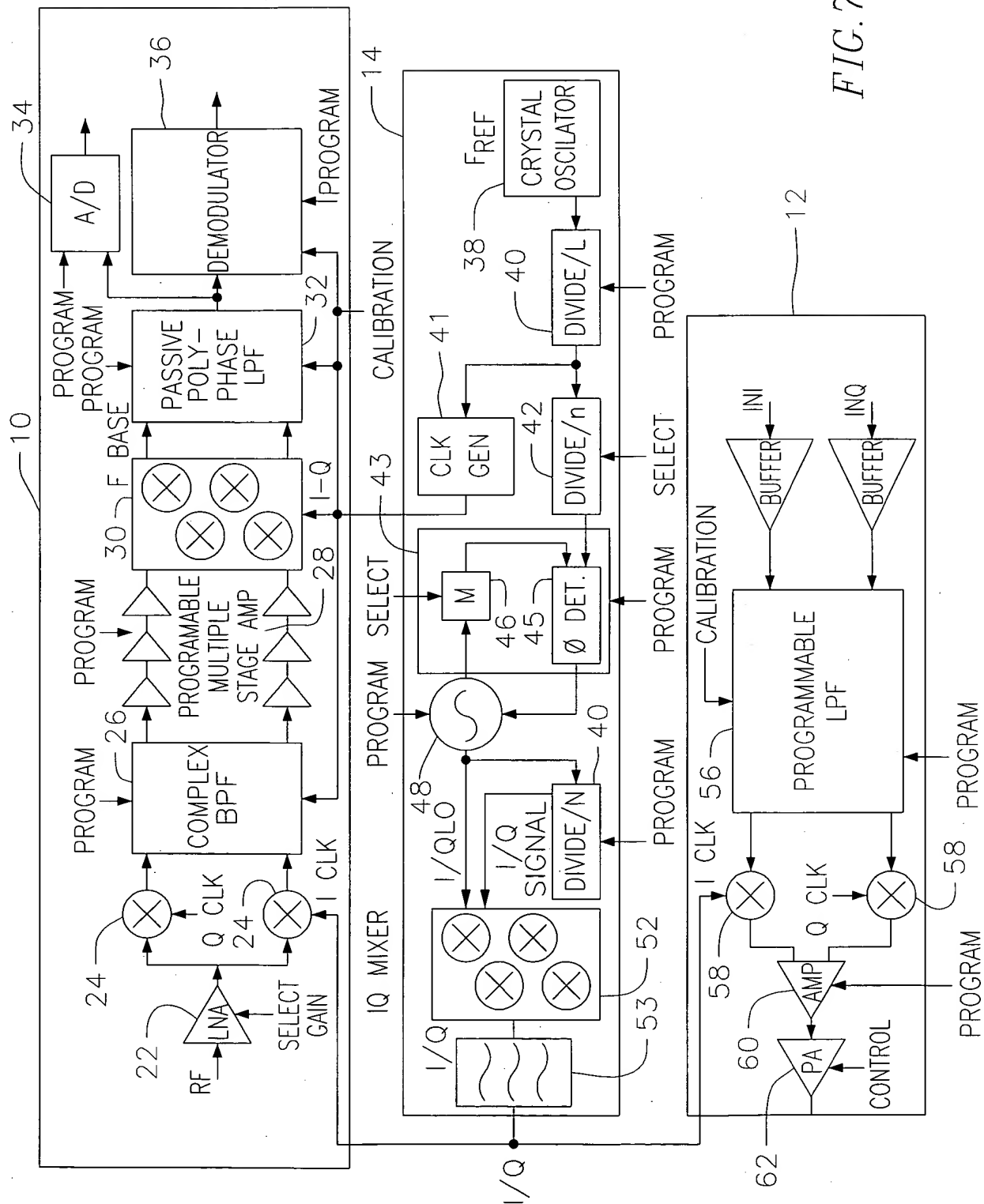


FIG. 7



FIG. 8

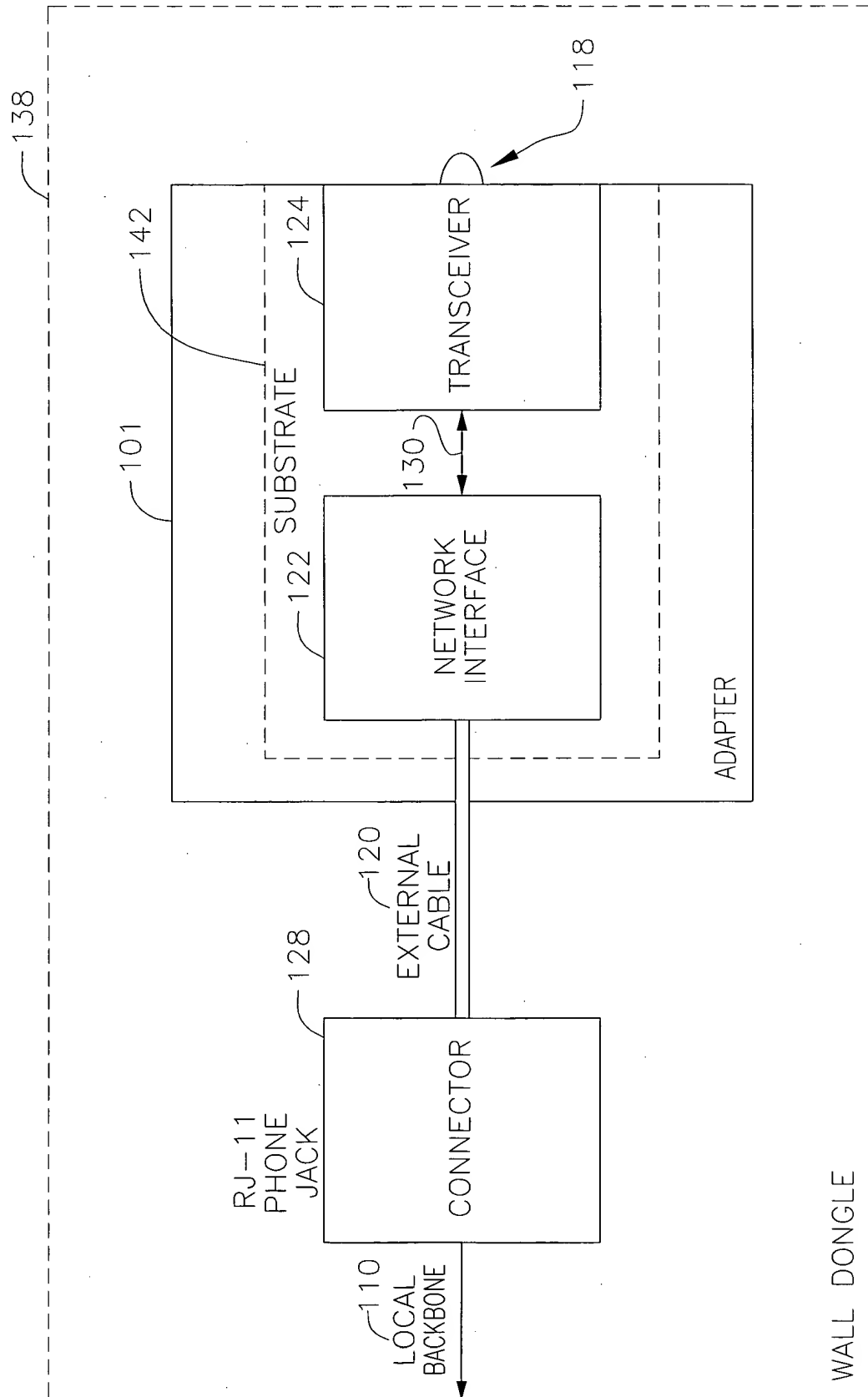






FIG. 9

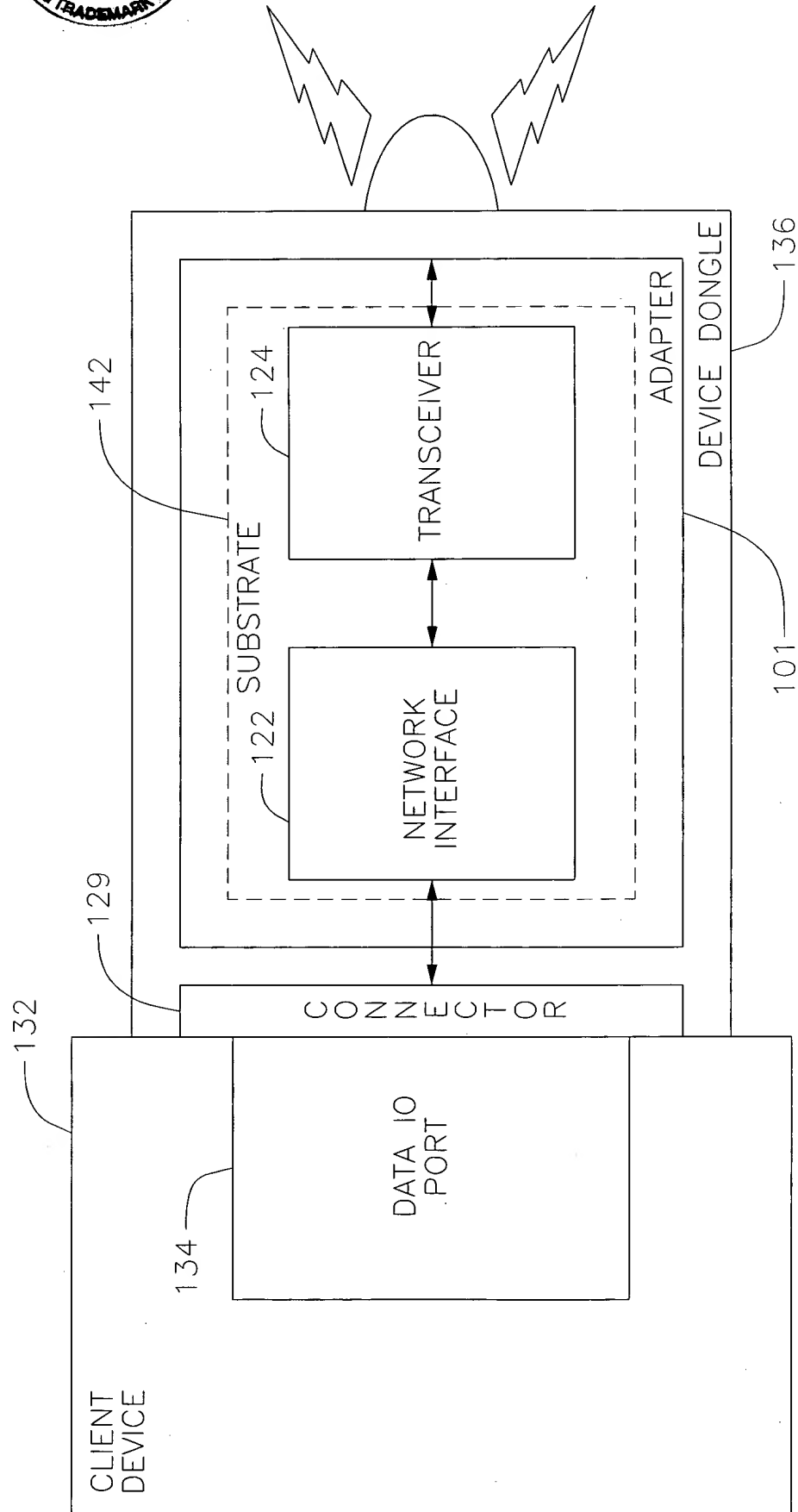




FIG. 10

